



UNITED STATES DEPARTMENT OF COMMERCE  
Patent and Trademark Office  
ASSISTANT SECRETARY OF COMMERCE AND  
COMMISSIONER OF PATENTS AND TRADEMARKS  
Washington, DC 20231

1/22/04

Patent No. :6909661  
Inventor(s) : Yang  
Issued : 6/21/05  
Title : SEMICONDUCTOR MEMORY DEVICE WITH DATA INPUT/OUTPUT  
ORGANIZATION IN MULTIPLES OF NINE BITS  
Atty.doc./File No.

**Request for Certificates of Correction**

Consideration has been given to your request for the issuance of a Certificate of Correction, for the above – identified patent under the provisions of CFR 1.322.

Inspection of the application for the patent reveals that there is no 1050 to make the correction please submit. Therefore being no fault on the Patent and Trademark Office, It has no authority to issue a certificate of correction under the provision of 1.322.

In view of the forgoing, your request in this matter, is hereby denied.

Future written correspondence concerning this matter should be filed and directed to Decisions & Certificates of Correction Branch.

Henry Randall  
Cecelia Newman  
Decisions & Certificates  
of Correction Branch  
(703) 308-9390 Ext. 108

MILLS & ONELLO LLP  
ELEVEN BEACON STREET, SUITE 605  
BOSTON, MA 02108

HR/CBN